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C 95 H**INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet

1 of 2

Complete if Known

Application Number	09/608,624
Filing Date	June 30, 2000
First Named Inventor	JOURDAN et al
Group Art Unit	2183
Examiner Name	H. Tsai

Attorney Docket Number 21207/8609

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
125		US- 6,076,144	06/13/00	Peled et al	
1		US- 6,351,844 B1	02/26/02	Bala	
125		US- 6,073,213	06/06/00	Peled	
125		US- 6,339,822 B1	01/15/02	Miller	

FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
125		Jourdan et al, "Extended Block Cache", Proceedings of the Sixth International Symposium on High-Performance Computer Architecture, Toulouse, France, January 8-12, 2000	
125		Hennessey & Patterson, <i>Computer Organization and Design</i> , 2nd Edition, page 570, 1998	
125		Rotenberg et al, "Trace Processors", Proceedings of 30th Annual IEEE/ACM International Symposium on Microarchitecture, pp. 138-148, December 1, 1997	

Examiner Signature

Date Considered

2/17/04

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